

**IGBT Modules** 

### Power Module (V series) 1200V / 225A / 2-in-1 package

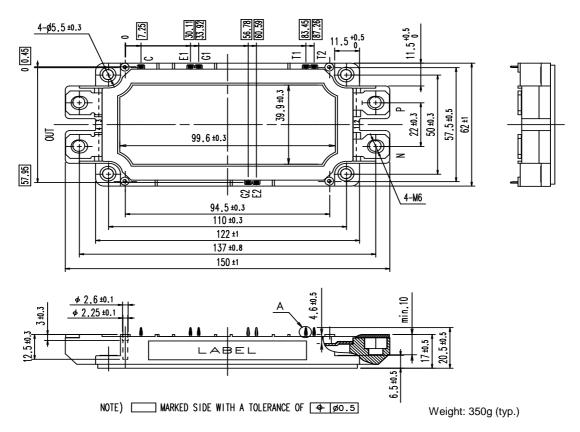
#### **■** Features

Low V<sub>CE(sat)</sub> Low Inductance Module structure Solderless press-fit terminals

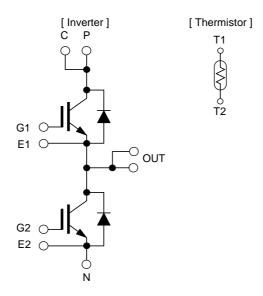
#### ■ Applications

Inverter for Motor Drives, AC and DC Servo Drives
Uninterruptible Power Supply Systems, Wind Turbines, PV Power Conditioning Systems

### ■ Outline drawing (Unit:mm)



#### **■** Equivalent Circuit



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### ■ Absolute Maximum Ratings (at T<sub>C</sub>= 25°C unless otherwise specified)

Items		Symbols	Conditions		Maximum Ratings	Units
Collector-	Emitter voltage	V <sub>CES</sub>			1200	V
Gate-Emit	ter voltage	$V_{GES}$			±20	V
Collector current		I <sub>C</sub>	Continuous	T <sub>C</sub> =25°C	300	
				T <sub>C</sub> =100°C	225	
		I <sub>C</sub> pulse	1ms	ns		Α
		-l <sub>C</sub>			225	
		-I <sub>C</sub> pulse	1ms		450	
Collector power dissipation		P <sub>C</sub>	1 device		1070	W
Junction temperature		T <sub>j</sub>			175	
Operating junction temperature		T <sub>jop</sub>			150	
(under switching conditions)					150	°C
Case temperature		T <sub>c</sub>			125	
Storage temperature		T <sub>stg</sub>			-40 ~ 125	
Isolation	between terminal and copper base (*1)	$V_{iso}$	AC: 1min.		2500	VAC
voltage	between thermistor and others (*2)	v iso	AC. IIIIII.		2500	VAC
Screw	Mounting (*3)	-			3.5	Nm
Torque	Terminals (*4)	-		·	4.5	INIII

<sup>(\*1)</sup> All terminals should be connected together during the test.

(\*3) Recommendable Value: 2.5-3.5 Nm (M5) (\*4) Recommendable Value: 3.5-4.5 Nm (M6)

<sup>(\*2)</sup> Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

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### ■ Electrical characteristics (at T<sub>j</sub>= 25°C unless otherwise specified)

Items	Symbole	Conditions		Characteristics			Units	
items	Symbols			min.	typ.	max.	Units	
Zero gate voltage Collector current	I <sub>CES</sub>	V <sub>GE</sub> =0V, V <sub>CE</sub> =1200V		-	-	3.0	mA	
Gate-Emitter leakage current	I <sub>GES</sub>	V <sub>CE</sub> =0V, V <sub>GE</sub> =±20V		-	-	600	nA	
Gate-Emitter threshold voltage	$V_{\text{GE(th)}}$	V <sub>CE</sub> =20V, I <sub>C</sub> =225mA		6.0	6.5	7.0	V	
	V <sub>CE(sat)</sub> (terminal)		T <sub>j</sub> =25°C	-	2.20	2.65	V	
			T <sub>j</sub> =125°C	-	2.55	-		
Collector-Emitter		$V_{GE} = 15V$	T <sub>j</sub> =150°C	-	2.60	-		
saturation voltage	V <sub>CE(sat)</sub> (chip)	I <sub>C</sub> = 225A	T <sub>j</sub> =25°C	-	1.85	2.30		
			T <sub>j</sub> =125°C	-	2.20	-		
			T <sub>i</sub> =150°C	-	2.25	-		
Internal gate resistance R <sub>G(int)</sub> -			-	3.33	-	Ω		
Input capacitance	_		=0V, f=1MHz	-	18	-	nF	
	t <sub>on</sub>			-	550	-		
Turn-on time	t <sub>r</sub>	V <sub>CC</sub> = 600V	I <sub>C</sub> = 225A	-	180	-		
	$t_{r(i)}$	$V_{GE} = \pm 15V$	$R_G = 1.6\Omega$	-	120	-	nsec	
Turn-off time	$t_{off}$	L <sub>s</sub> = 80nH		-	1050	-		
Turr-on time	t <sub>f</sub>			-	110	-		
	V <sub>F</sub> (terminal)	V <sub>GE</sub> = 0V	T <sub>j</sub> =25°C	-	2.05	2.50	- V	
			T <sub>j</sub> =125°C	-	2.20	-		
Forward on voltage			T <sub>j</sub> =150°C	-	2.15	-		
	Voltage V <sub>F</sub> (chip)	I <sub>F</sub> = 225A	T <sub>i</sub> =25°C	-	1.70	2.15		
			T <sub>i</sub> =125°C	-	1.85	-		
			T <sub>i</sub> =150°C	-	1.80	-	-	
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 225A	,	-	200	-	nsec	
Thermistor Resistance	esistance R	T=25°C		-	5000	-	Ω	
THEITHSIOI NESISIANCE		T=100°C		465	495	520		
Thermistor B value	В	T=25/50°C		3305	3375	3450	K	

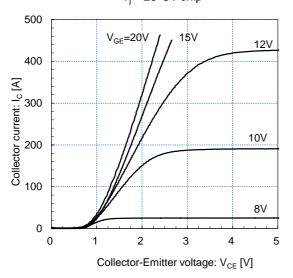
### 5. Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
items		Conditions	min.	typ.	max.	Ullits
Thermal resistance	D	IGBT	-	-	0.14	
(1device)	$R_{th(j-c)}$	FWD	-	-	0.19	°C/W
Contact thermal resistance 1device) (*1)		with thermal compound	-	0.0167	-	- C/VV

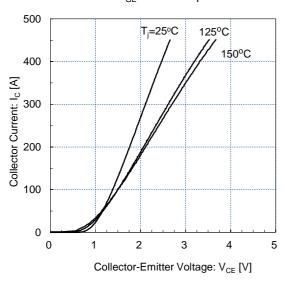
<sup>(\*1)</sup> This is the value which is defined mounting on the additional cooling fin with thermal compound.

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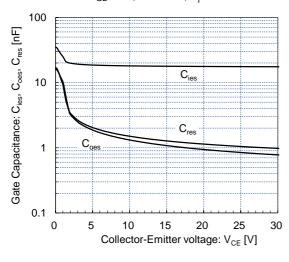
Collector current vs. Collector-Emitter voltage  $T_i = 25^{\circ}C$  / chip



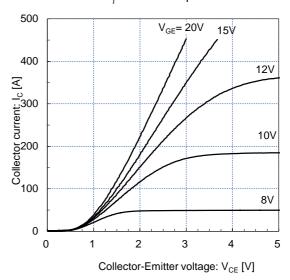
Collector current vs. Collector-Emitter voltage  $V_{GE} = 15V / chip$ 



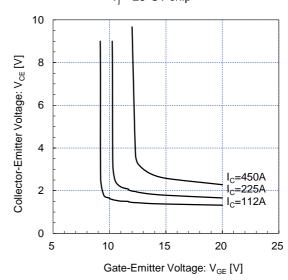
Capacitance vs. Collector-Emitter Voltage  $V_{GE}$ = 0V, f= 1MHz,  $T_i$ = 25°C



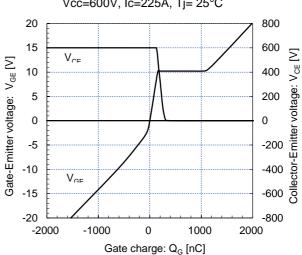
Collector current vs. Collector-Emitter voltage (typ.)  $T_i = 150^{\circ}C$  / chip



Collector-Emitter voltage vs. Gate-Emitter voltage  $T_i = 25^{\circ}C$  / chip

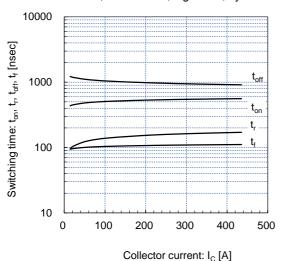


Dynamic Gate Charge (typ.) Vcc=600V, Ic=225A, Tj= 25°C

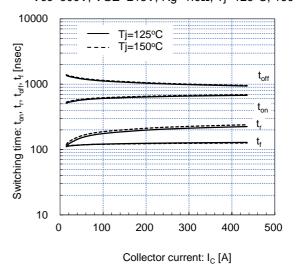


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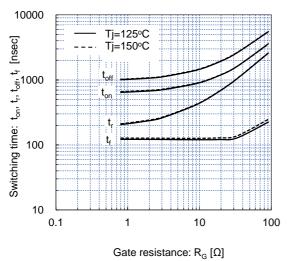
Switching time vs. Collector current (typ.) Vcc=600V, VGE=±15V, Rg=1.6Ω, Tj=25°C



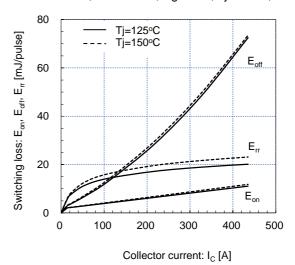
Switching time vs. Collector current (typ.) Vcc=600V, VGE=±15V, Rg=1.6Ω, Tj=125°C, 150°C



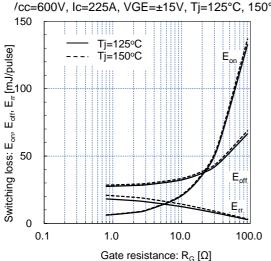
Switching time vs. Gate resistance (typ.) /cc=600V, Ic=225A, VGE=±15V, Tj=125°C, 150°(



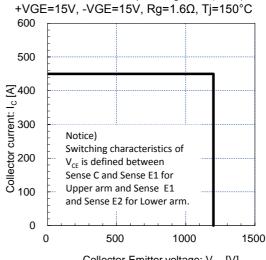
Switching loss vs. Collector current (typ.) Vcc=600V, VGE=±15V, Rg=1.6Ω, Tj=125°C, 150°C



Switching loss vs. Gate resistance (typ.) /cc=600V, Ic=225A, VGE=±15V, Tj=125°C, 150°(



Reverse bias safe operating area (max.)

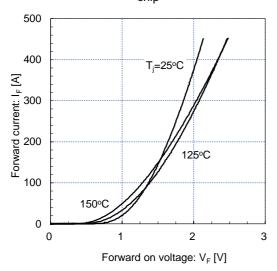


Collector-Emitter voltage: V<sub>CE</sub> [V]

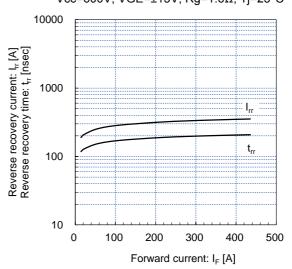
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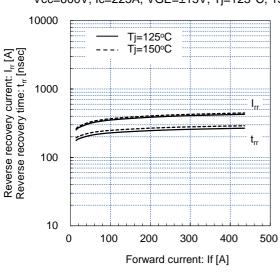
Forward current vs. Forward vltage (typ.)



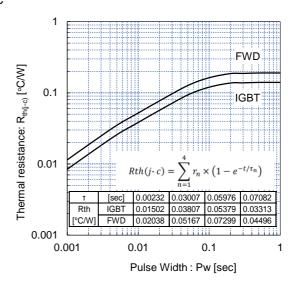
Reverse recovery characteristics (typ.) Vcc=600V, VGE=±15V, Rg=1.6Ω, Tj=25°C



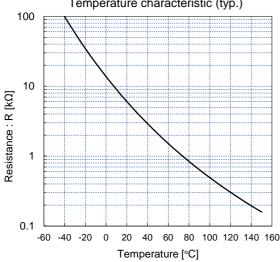
Reverse Recovery Characteristics (typ.) Vcc=600V, Ic=225A, VGE=±15V, Tj=125°C, 150°C



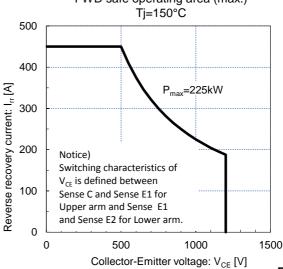
Transient Thermal Resistance (max.)



[THERMISTOR] Temperature characteristic (typ.)



FWD safe operating area (max.)



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